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Attorney Docket SEL 171

#16/ Jmatt C
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Kadono et al.

Serial No.: 09/535,233

Filed: March 24, 2000

For: A Method Of Manufacturing A
Semiconductor Device

Art Unit: 2823

Examiner: W. Coleman

Commissioner for Patents
Washington, D.C. 20231

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Commissioner for Patents
Washington, D.C. 20231

on May 28, 2002

Rachelle Hammerquist

Rachelle Hammerquist
Signature Date 5/28/02

AMENDMENT C

Sir:

In response to the Final Rejection dated March 26, 2002, a RCE being submitted herewith,
please amend the above-identified application as follows:

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IN THE CLAIMS:

Please amend the claims as follows:

11. (Amended) A method of manufacturing a semiconductor device, comprising steps of:

forming a semiconductor film formed over a substrate;

spinning the substrate;

contacting an etching solution to a surface of said semiconductor film and scattering the
etching solution during said spinning, thereby contaminating impurities are removed from the surface;

and then